

Amendments to the Specification:

Page 9, please replace the first full paragraph with the following amended paragraph:

(2) The multi-layer printed circuit board of item 1, wherein a snubber circuit, being a serial connection of a ~~resister~~ resistor and capacitor, is coupled between the ground layer and the electronic power source layer.

Please replace the paragraph bridging **pages 11-12** with the following amended paragraph:

(8) A method for installing a multi-layer printed circuit board on an electro-conductive housing, the multi-layer printed circuit, on which electronic parts are mounted, comprising a first signal layer formed on an obverse surface of the multi-layer printed circuit board, a ground layer arranged at a position next to the first signal layer, an electronic power source layer arranged at a position next to the ground layer, and a second signal layer formed on a reverse surface of the

multi-layer printed circuit board, the method comprising the steps of: forming a first ground pattern around a peripheral area of the first signal layer; forming a second ground pattern around a peripheral area of the second signal layer; coupling electrically the first ground pattern to the second ground pattern with a plurality of through holes; and mounting the multi-layer printed circuit board on a mounting area of the electro-conductive housing in such a manner that a substantially whole area of the first ground ~~pattern~~ pattern is electrically coupled to the electroconductive housing through an electro-conductive member.

Page 19, please replace the fifth full paragraph with the following amended paragraph:

Figs. 12(a), Fig. 12(b), Fig. 12(c) and ~~Fig. 12(a)~~ Fig. 12(d) show cross sectional views of the printed circuit board mounted on the electro-conductive housing corresponding to other various kinds of installing methods; and

Please replace the paragraph bridging **pages 23-24** with the following amended paragraph:

As shown in Figs. 11(a) - 11(c), a plurality of ~~snubber~~ snubber circuits, each of which is a serial connection of ~~resister~~ resistor R and capacitor C, are mounted between power source layer 13 and signal layers 14, 15 around the circumferential area of printed circuit board 1. Generally speaking, the mounting distance between the ~~snubber~~ snubber circuits is several centimeters. This ~~snubber~~ snubber circuit is effective for preventing an abrupt change of the electronic current flowing into the electronic parts, resulting in a suppression of the resonance, a reduction of the common mode current flowing out from electro-conductive housing 2 and a reduction of the electromagnetic noise emission.

Replace the paragraph bridging **pages 29-30** with the following amended paragraph:

(3) By coupling the electronic power source layer power source layer to the ground layer with a plurality of snubber circuits, each being a serial connection of a ~~resister~~ resistor and a capacitor, it becomes possible to prevent not only an occurrence of the resonance between the electronic power source layer and the ground layer, but also the common mode current flowing into the electro-conductive housing, resulting in a reduction of the electro-magnetic noise emission.